

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Takashi KOBAYASHI et al.

Appln. No.:

Filed: HEREWITH

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND  
METHOD OF MANUFACTURING THEREOF

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

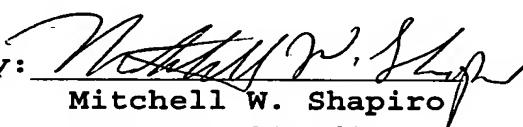
Pursuant to 37 C.F.R. § 1.56, and without any  
assertion as to materiality or prior art effect, the  
documents listed on the attached Form PTO-1449 are hereby  
cited.

The documents on the attached List were cited in the  
specification, on pages 1, 3 and 23, and their relevance is  
indicated therein.

Respectfully submitted,

MWS:skj

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By:   
Mitchell W. Shapiro  
Reg. No. 31,568

November 26, 2003

FORM PTO-1449				Atty. Docket No. <b>XA-10000</b>	Appn. No.			
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>				Applicant <b>Takashi KOBAYASHI et al.</b>				
				Filing Date <b>HEREWITH</b>	Group			
<b>U.S. PATENT DOCUMENTS</b>								
Examiner Initial		Document Number	Date	Name		Class	Sub-class	Filing Date
	AA	<b>6,011,725</b>	<b>01/04/00</b>	<b>Eitan</b>		<b>365</b>	<b>185.33</b>	
	AB	<b>5,966,603</b>	<b>10/12/99</b>	<b>Eitan</b>		<b>438</b>	<b>258</b>	
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
<b>FOREIGN PATENT DOCUMENTS</b>								
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation	
	AJ	<b>5-75133</b>	<b>03/26/93</b>	<b>Japan</b>			<b>Abstract</b>	
	AK							
	AL							
	AM							
	AN							
	AO							
<b>OTHER</b> (including author, title, date, pertinent pages, etc.)								
	AP							
	AQ							
	AR							
Examiner				Date Considered				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.								